



U2
ST8105

1	NC	11	GNDSENSE	21	VDD	31	XRES	41	XWR	50	NC
2	KVM2	12	VSS	22	VDD	32	D7	42	XCS1		
3	SCLK_OUT	13	Vpp	23	PXS	33	D6	43	XCS2		
4	FSYNC_OUT	14	VH2	24	TONXF	34	D5	44	VSS		
5	LSYNC_OUT	15	VH1	25	CMODE	35	D4	45	FSYNC_IN		
6	VSENSE	16	AVM2	26	SELCLK	36	D3	46	CRG		
7	VCOMP	17	AVM1	27	MSELK	37	D2	47	CRR		
8	ISENSE	18	VF	28	MSELO	38	D1	48	CRC		
9	VDC	19	VREF2	29	VDD	39	D0	49	KVM1		
10	GATE	20	VREF1	30	XRES	40	XCMD				
11	VDRIVE	21	VDD	31	XRES	41	XWR				
12	GNDSENSE	22	VDD	32	D7	42	XCS1				
13	Vpp	23	PXS	33	D6	43	XCS2				
14	VH2	24	TONXF	34	D5	44	VSS				
15	VH1	25	CMODE	35	D4	45	FSYNC_IN				
16	AVM2	26	SELCLK	36	D3	46	CRG				
17	AVM1	27	MSELK	37	D2	47	CRR				
18	VF	28	MSELO	38	D1	48	CRC				
19	VREF2	29	VDD	39	D0	49	KVM1				
20	VREF1	30	XRES	40	XCMD						
21	VDD	31	XRES	41	XWR						
22	VDD	32	D7	42	XCS1						
23	PXS	33	D6	43	XCS2						
24	TONXF	34	D5	44	VSS						
25	CMODE	35	D4	45	FSYNC_IN						
26	SELCLK	36	D3	46	CRG						
27	MSELK	37	D2	47	CRR						
28	MSELO	38	D1	48	CRC						
29	VDD	39	D0	49	KVM1						
30	XRES	40	XCMD								
31	XRES	41	XWR								
32	D7	42	XCS1								
33	D6	43	XCS2								
34	D5	44	VSS								
35	D4	45	FSYNC_IN								
36	D3	46	CRG								
37	D2	47	CRR								
38	D1	48	CRC								
39	D0	49	KVM1								
40	XCMD										
41	XWR										
42	XCS1										
43	XCS2										
44	VSS										
45	FSYNC_IN										
46	CRG										
47	CRR										
48	CRC										
49	KVM1										
50	NC										

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